



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#13/B  
LTY8007  
12-20-02

In re PATENT application of )  
Jun KOYAMA et al. )  
Serial No. 09/448,756 ) Art Unit: 2675  
Filed: November 24, 1999 ) Examiner: Paul Bell  
For: REDUNDANCY SHIRT REGISTER) Date: December 2, 2002  
CIRCUIT FOR DRIVER CIRCUIT )  
IN ACTIVE MATRIX TYPE... )

AMENDMENT

Honorable Commissioner for Patents  
Washington, D.C. 20231

Sir:

CERTIFICATE OF MAILING  
I hereby certify that this  
correspondence is being deposited with  
the United States Postal Service with  
sufficient postage as First Class Mail in  
an envelope addressed to: Assistant  
Commissioner for Patents, Washington,  
D.C. 20231, on 12/2/02  
*[Signature]*

In response to the Office Action mailed July 31, 2002, please amend the above  
identified application as follows.

IN THE SPECIFICATION:

The paragraph below is presented below in its amended form. The amendments  
to the specification are outlined in an Attachment to the Amendment using the  
conventional indication method of bracketing and underlining.

On page 5, please replace the third full paragraph with the following:

B1  
--As shown in Fig. 1, a shift register circuit 104 includes at least one register  
group 103 and a register selecting switch 102. The register group includes register lines  
101a, 101b, ..., and 101n having a plurality of serial-connected registers SRAi,  
SRBi, ..., and SRZi (i = 1 to n), respectively. The register line selecting switch 102  
selects one of the register lines.--